

MAIL STOP AMENDMENT

JFM

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J.-S. Mok et al. Attorney Docket No.: LEPA122042

Application No.: 10/717,977 Art Unit: 2822 / Confirmation No.: 8002

Filed: November 20, 2003 Examiner: M.M. Trinh

Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING
IMPROVED INTERCONNECTION AND METHOD FOR
MANUFACTURING THE SAME

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Seattle, Washington 98101

August 3, 2006

TO THE COMMISSIONER FOR PATENTS:

Applicants are aware of the information listed in the attached form that may be material to the prosecution of the above-identified patent application.

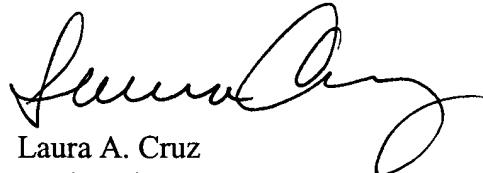
1. Copies of the listed foreign patents, their abstracts, and the July 18, 2006 Japanese Office Action are enclosed for the Examiner's use.
2. A concise explanation of the relevance of document Cite Nos. F2 through F7 (which are not in the English language), as presently understood by the individual designated under 37 C.F.R. § 1.56(c) most knowledgeable about its content, is provided in an English abstract appended to said document.
3. Pursuant to 37 C.F.R. § 1.704(d), this Information Disclosure Statement is accompanied by a statement that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart application. Such communication was received by an individual designated in 37 C.F.R. § 1.56(c) on July 18, 2006, which is not more than thirty days prior to the filing of this Information Disclosure Statement.

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100

4. X The Commissioner is hereby authorized to charge any fees which may be required or credit any overpayment to Deposit Account No. 03-1740.

Respectfully submitted,

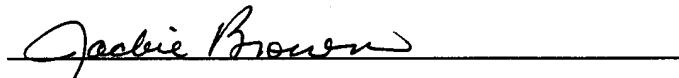
CHRISTENSEN O'CONNOR
JOHNSON KINDNESS^{PLLC}



Laura A. Cruz
Registration No. 46,649
Direct Dial No. 206.695.1725

I hereby certify that this correspondence is being deposited with the U.S. Postal Service in a sealed envelope as first-class mail with postage thereon fully prepaid and addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date.

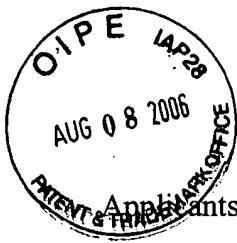
Date: 8/3/06



LXC:jlb

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100

MAIL STOP AMENDMENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: J.-S. Mok et al. Attorney Docket No.: LEPA122042

Application No.: 10/717/977 Art Unit: 2822 / Confirmation No.: 8002

Filed: November 20, 2003 Examiner: M.M. Trinh

Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING
IMPROVED INTERCONNECTION AND METHOD FOR
MANUFACTURING THE SAME

CERTIFICATION STATEMENT UNDER 37 C.F.R. § 1.704(d)

August 3, 2006

TO THE COMMISSIONER FOR PATENTS:

- I, the person signing below, state that each item of information contained in the accompanying information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application and this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this information disclosure statement.
- I, the person signing below, am the attorney who prepared or prosecuted the application, signing on the basis of the information supplied by another individual designated in 37 C.F.R. § 1.56(c).

Respectfully submitted,

CHRISTENSEN O'CONNOR
JOHNSON KINDNESS^{PLLC}

Laura A. Cruz
Registration No. 46,649
Direct Dial No. 206.695.1725

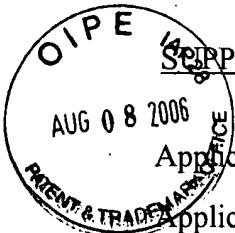
I hereby certify that this correspondence is being deposited with the U.S. Postal Service in a sealed envelope as first-class mail with postage thereon fully prepaid and addressed to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date.

Date:

8/3/06

LXC:jlb

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100



**SUPPLEMENTAL INFORMATION CITED BY APPLICANTS THAT MAY BE MATERIAL
TO THE PROSECUTION OF THE SUBJECT APPLICATION**

Applicants: J.-S. Mok et al. Attorney Docket No.: LEPA122042

Application No.: 10/717,977 Art Unit: 2822 / Confirmation No.: 8002

Filed: November 20, 2003 Examiner: M.M. Trinh

Title: PARALLEL MULTI-LAYER PRINTED CIRCUIT BOARD HAVING
IMPROVED INTERCONNECTION AND METHOD FOR
MANUFACTURING THE SAME

U.S. PATENT DOCUMENTS

None.

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Cite No.	Document No.	Kind Code	Publication Date (mm/dd/yyyy)	English	
					Abstract Provided	Translation Provided
_____	F2	JP 02-288299	_____	11/28/1990	JP	X
_____	F3	JP 07-147464	_____	06/06/1995	JP	X
_____	F4	JP 10-224039	_____	08/21/1998	JP	X
_____	F5	JP 11-112118	_____	04/23/1999	JP	X
_____	F6	JP 2002-016358	_____	01/18/2002	JP	X
_____	F7	JP 2003-224339	_____	08/08/2003	JP	X

OTHER INFORMATION

None.

Examiner

Date Considered

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

LXC:jlb